

the EL display of the present invention;

Fig. 22 is a cross-sectional view of the EL display of the present invention;

Fig. 23 is a photograph showing a top view of the EL display of the present invention;

5 Fig. 24 is a diagram illustrating a driving method of the EL display of the present invention;

Figs. 25A and 25B are top views of the EL display of the present invention;

Fig. 26A is a diagram showing a connecting configuration between an EL element and an EL driving TFT, and Fig. 26B is a diagram showing a voltage-current characteristic of the EL element and the EL driving TFT;

10 Fig. ²⁷~~27B~~ is a diagram showing a voltage-current characteristic of the EL element and the EL driving TFT; and

Fig. 28 is a diagram illustrating a relation between a gate electrode of an EL driving TFT and a drain current.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Hereinafter, a description of a structure and a driving method of an EL display of the present invention will be made. A case of performing a 2" gray-scale by an n-bit digital data signal will be explained here.

20 Fig. 1 is a block diagram showing an example of an EL display of the present invention. The EL display of Fig. 1 comprises a pixel portion 101, a source signal line driver circuit 102 arranged in the periphery of the pixel portion 101, a writing-in gate signal line driver circuit (a first gate signal line driver circuit) 103, and an eliminating gate signal line driver circuit (a second gate signal line driver circuit) 104 formed of
25 TFTs formed on a substrate. Note that although the EL display has one source signal